

In the Claims:

Please cancel claims 1-20 without any disclaimer or a prejudice, add new claims 21-24 as follows:

1-20 (Cancelled)

21. (Newly Added) A thin film transistor array panel for a liquid crystal display, comprising:

a gate line formed in a first direction;

a data line formed in a second direction, wherein the data line intersects the gate line is insulated from the gate line;

a pixel electrode formed in a pixel defined by an intersection of the gate line and the data line; and

a conductive pattern that is connected to the data line and shaped like a ladder,

wherein the first direction is not parallel with the second direction.

22. (Newly Added) The thin film transistor array panel of claim 21, further comprising:

a storage wire including a storage electrode line,

wherein the storage electrode line connects the storage electrode of a neighboring pixel, and

wherein the pixel electrode forms a storage capacitance by overlapping the storage wire and receives image signals through the data line.

23. (Newly Added) The thin film transistor array panel of claim 21, further comprising:
a passivation layer formed between the pixel electrode and the data line.

24. (Newly added) A thin film transistor array panel for a liquid crystal display,
comprising:
a gate line formed in a first direction;
a common wire including a common electrode line that is connected to a common
electrode;
a data line formed in a second direction, wherein the data wire intersects the gate line and
is insulated from the gate line;
a pixel electrode formed in a pixel defined by an intersection of the gate line and the data
line, wherein the pixel electrode forms a storage capacitance by overlapping the common wire
and receives image signals through the data line; and
a conductive pattern that is connected to the data line and is shaped like a ladder,
wherein the first direction is not parallel with the second direction.